

FIG. 1

10

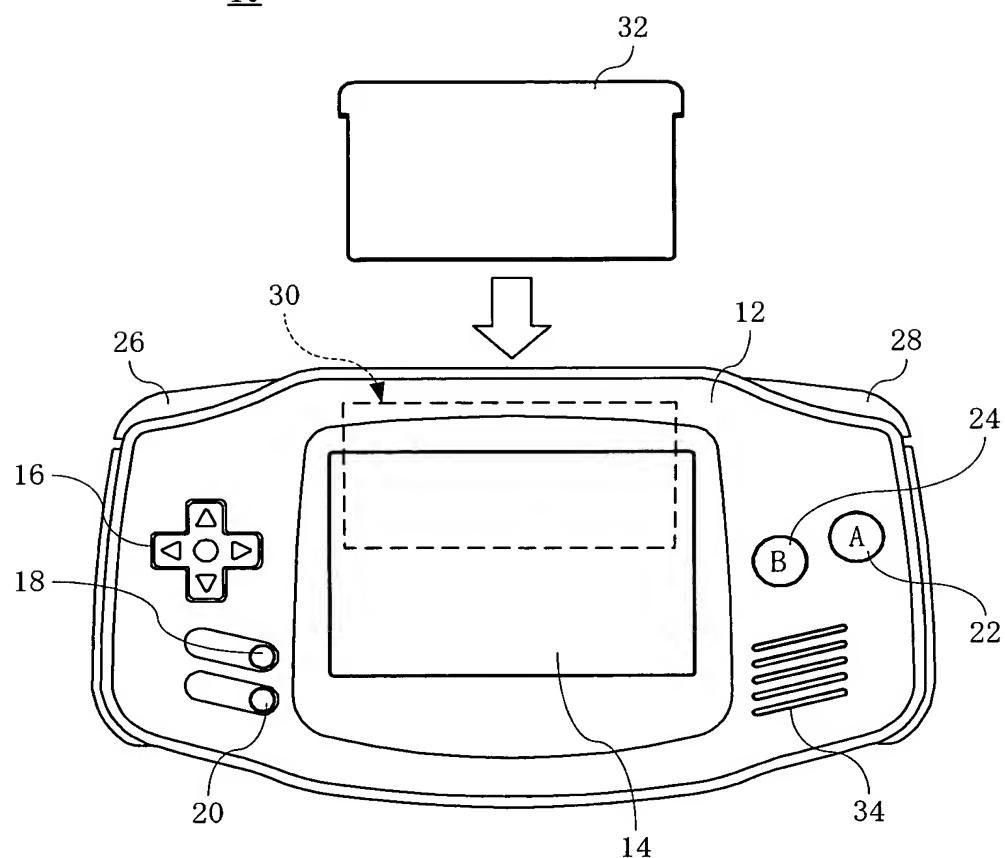


FIG. 2

10

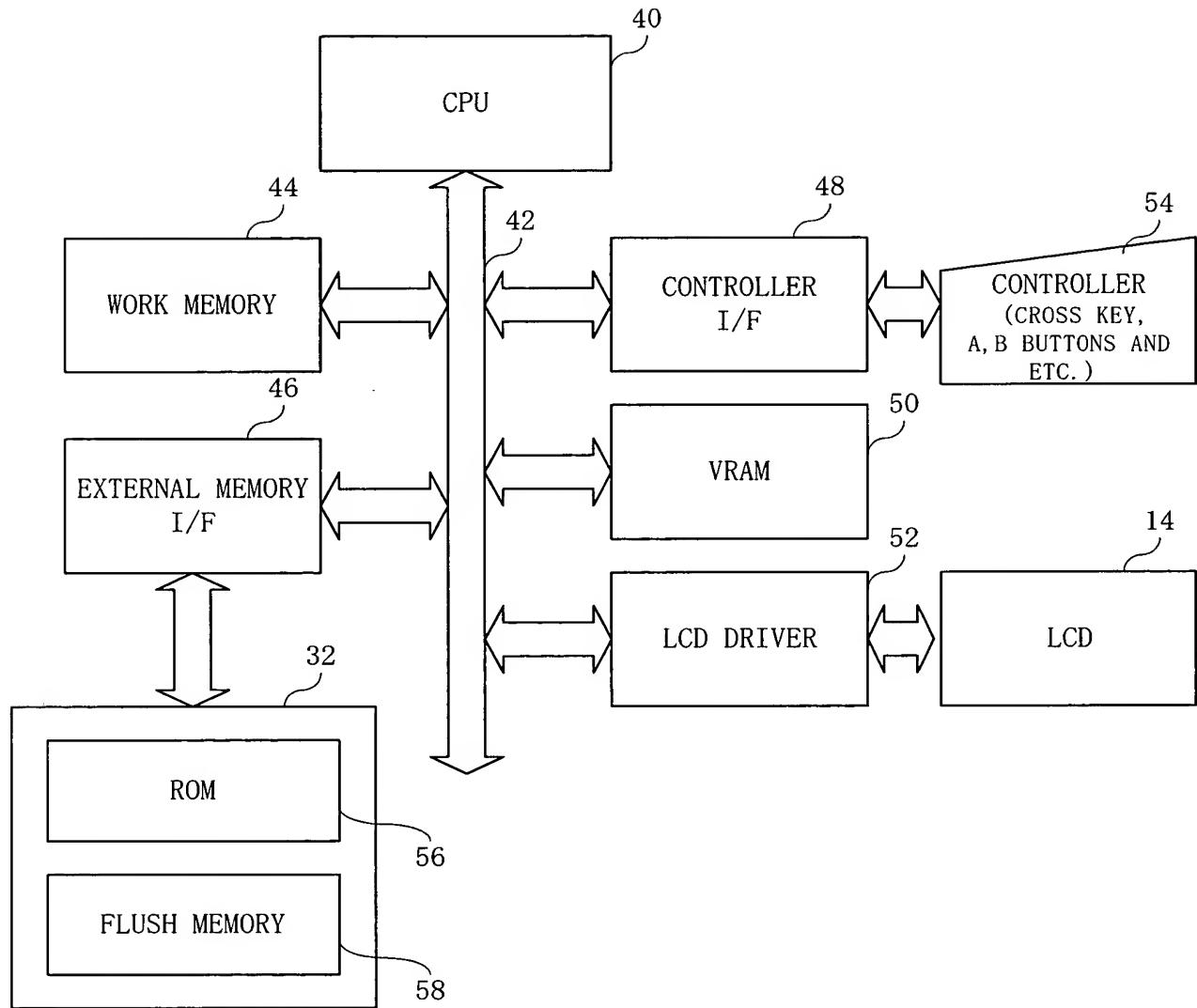


FIG. 3

56

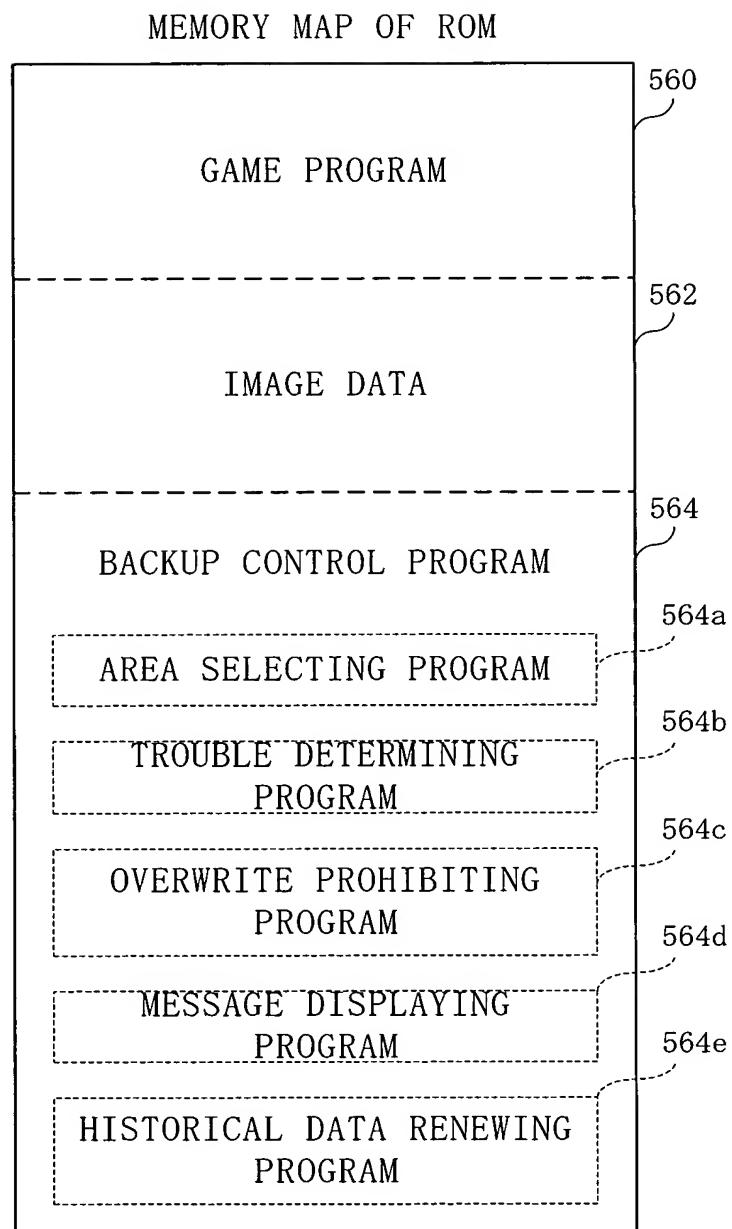
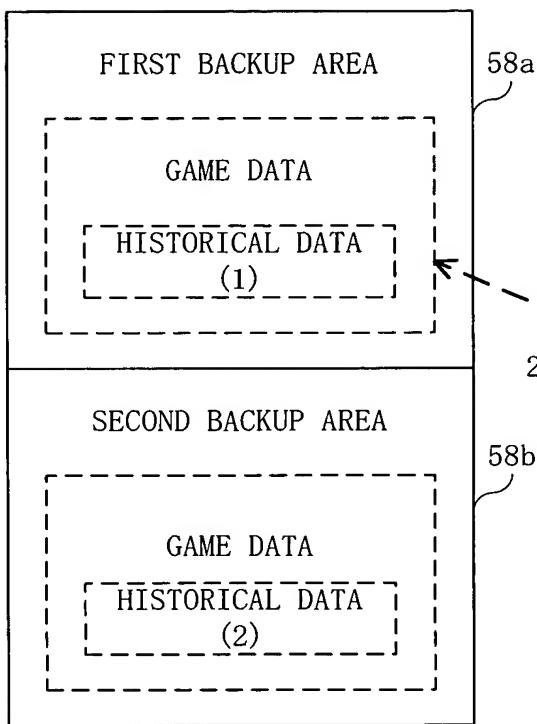


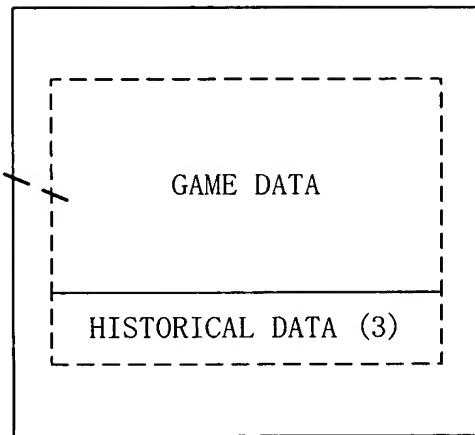
FIG. 4

(A)

58
MEMORY MAP

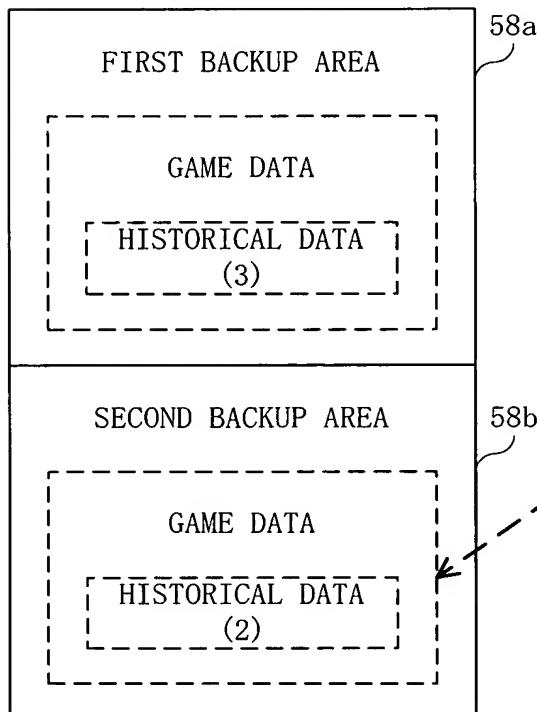


44
MEMORY MAP



(B)

58
MEMORY MAP



44
MEMORY MAP

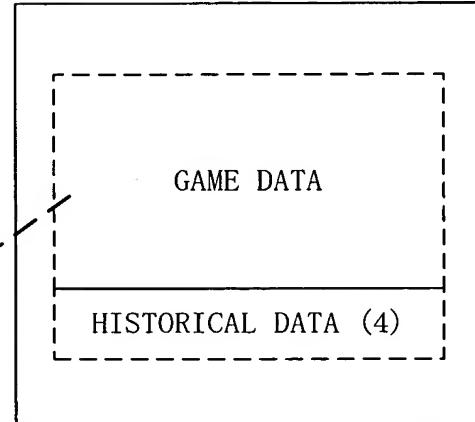
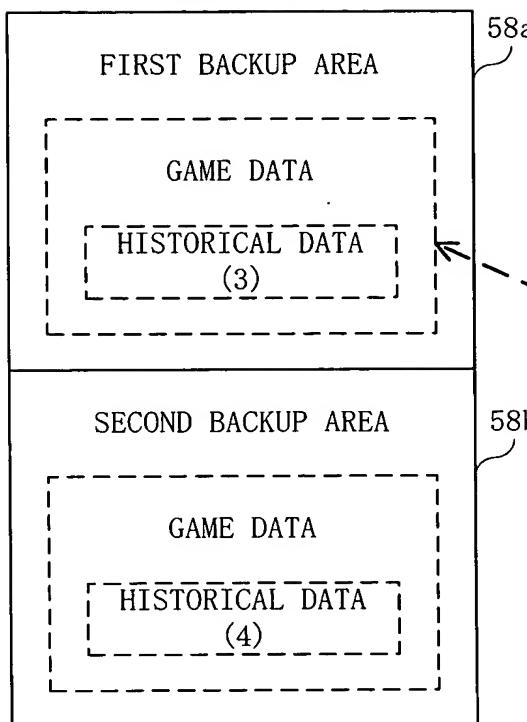


FIG. 5

(A)

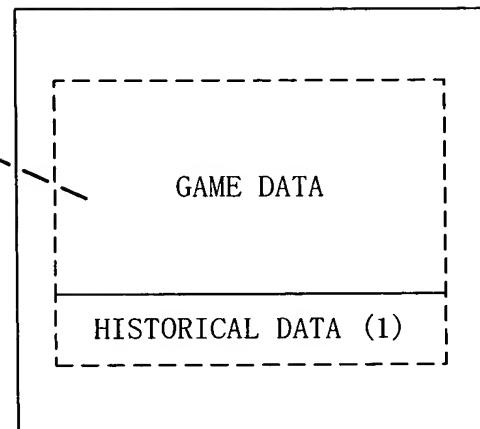
58

MEMORY MAP



44

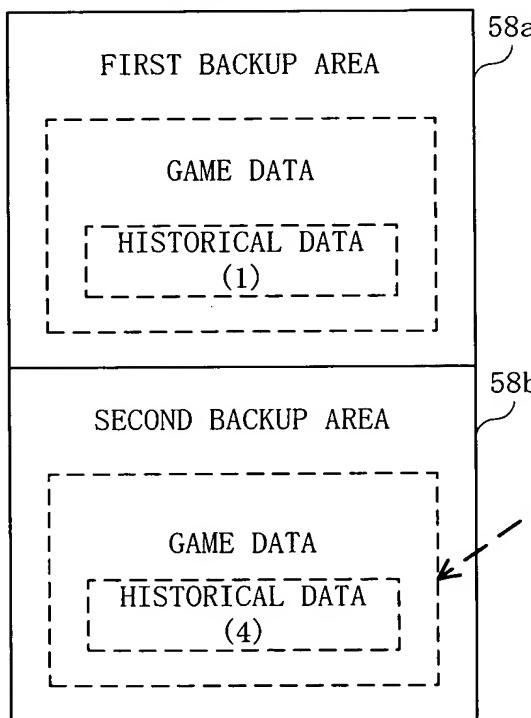
MEMORY MAP



(B)

58

MEMORY MAP



44

MEMORY MAP

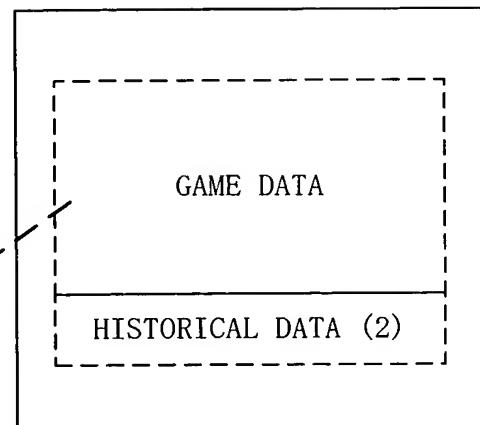
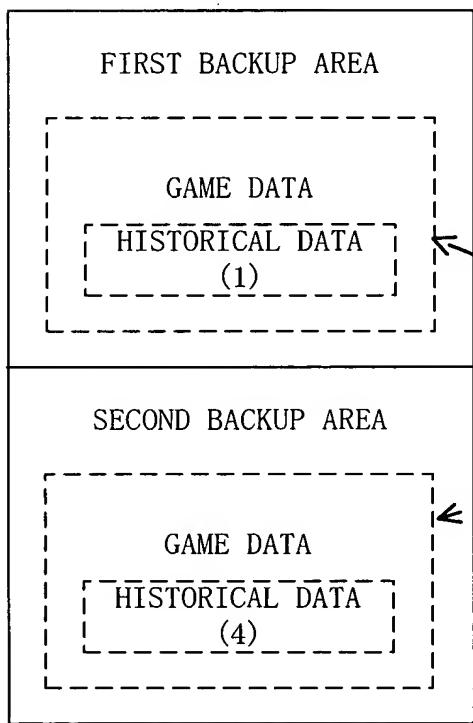


FIG. 6

58

MEMORY MAP



44

MEMORY MAP

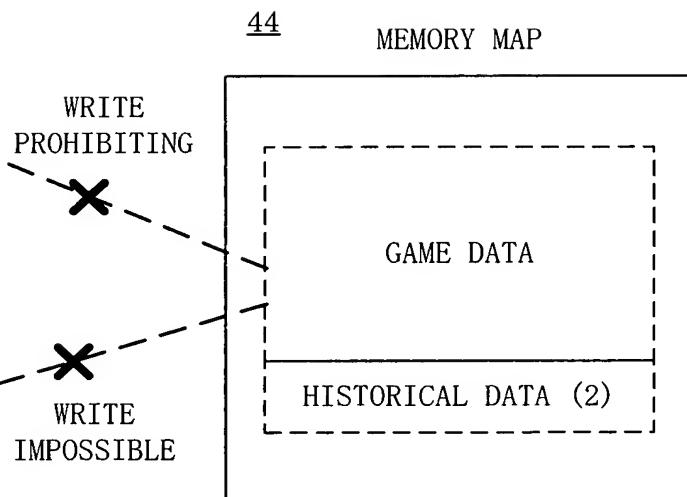
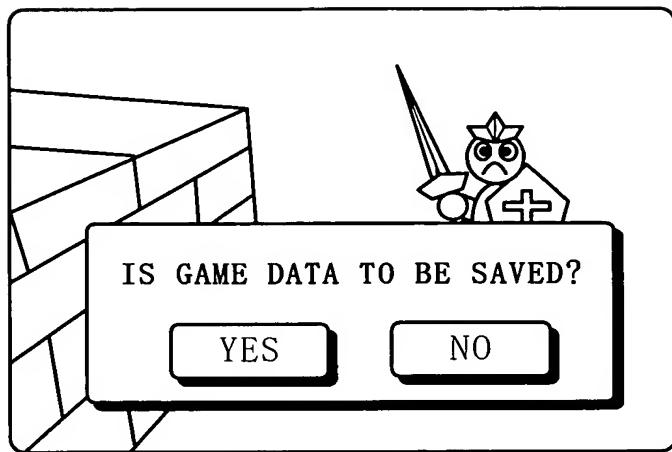
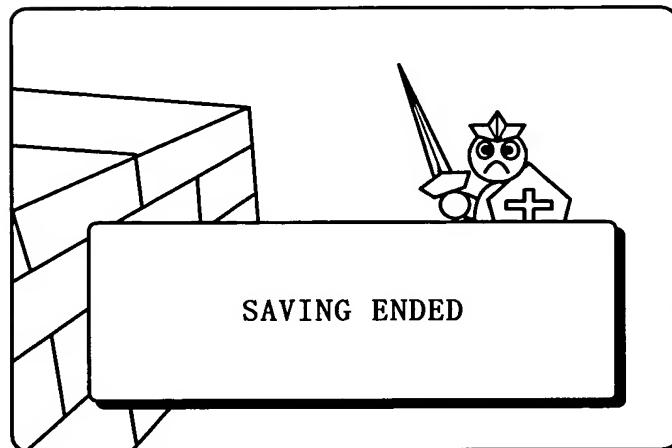


FIG. 7

(A)



(B)



(C)

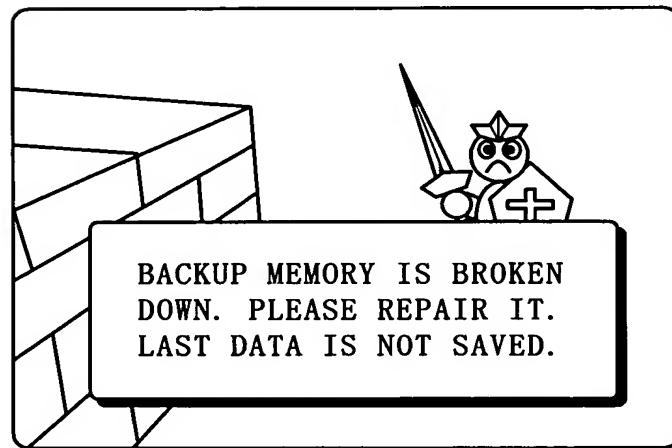
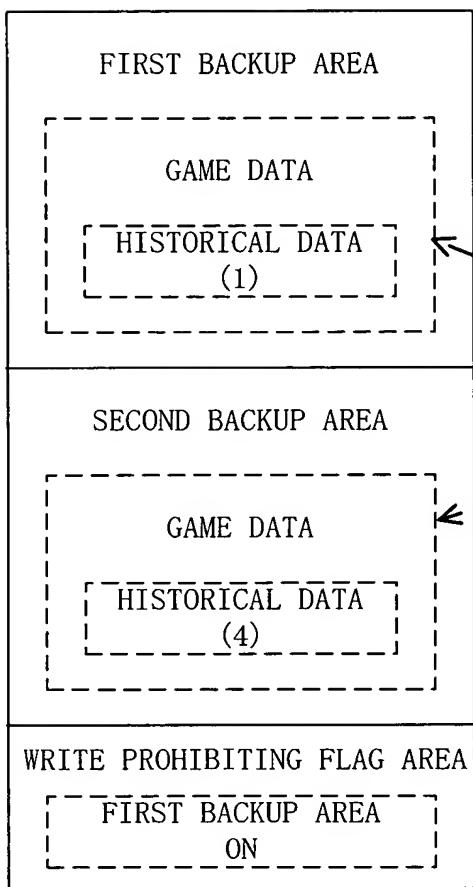


FIG. 8

58

MEMORY MAP



44

MEMORY MAP

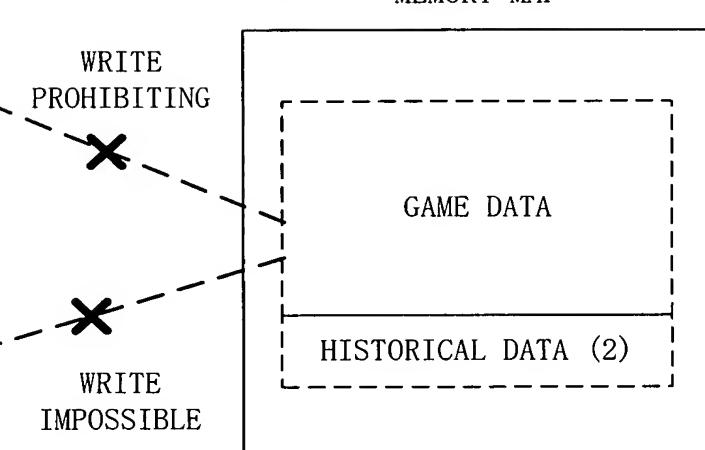


FIG. 9

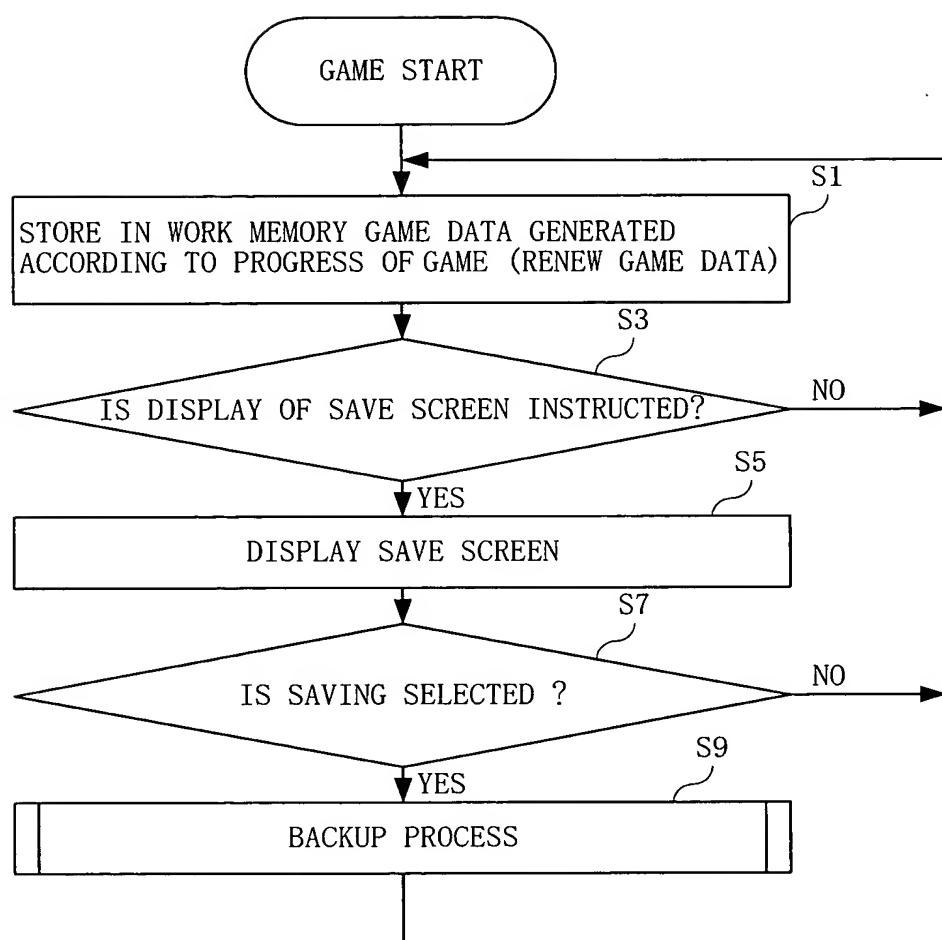


FIG. 10

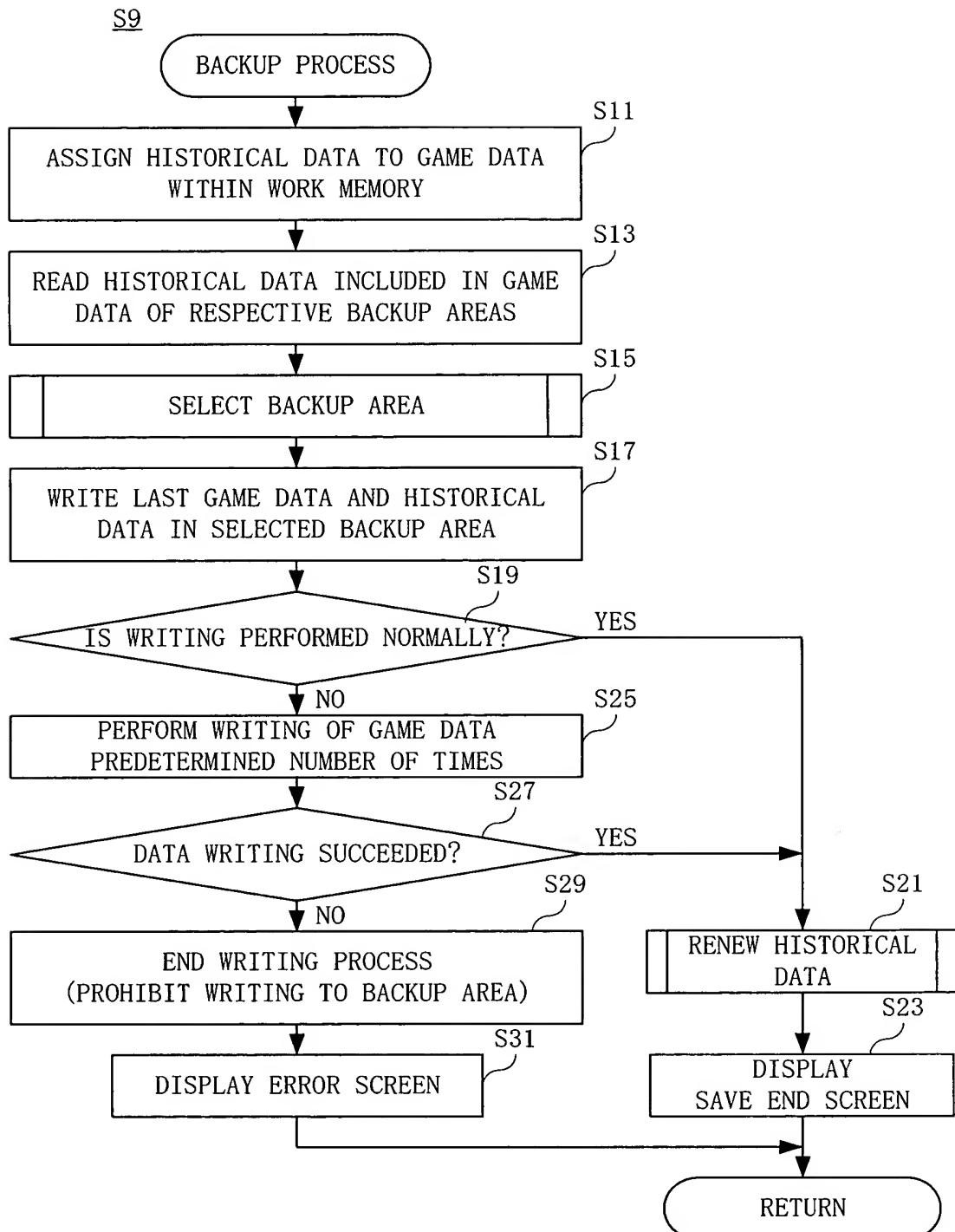


FIG. 11

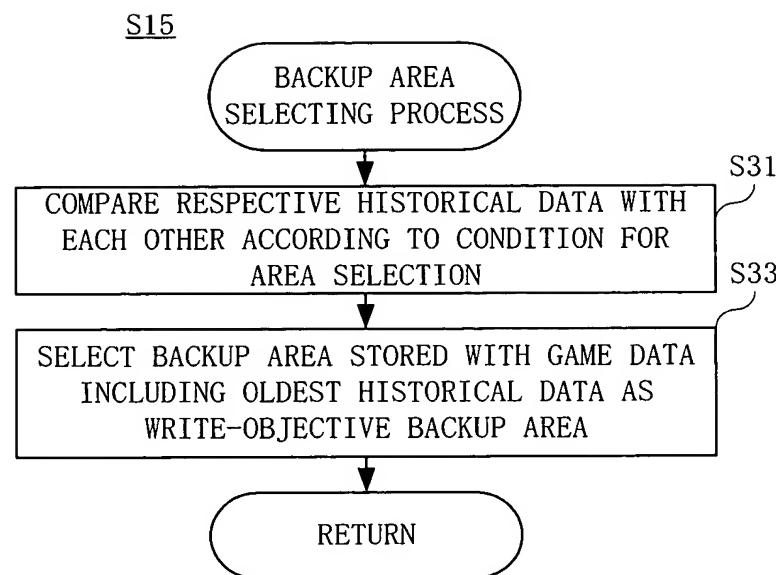


FIG. 12

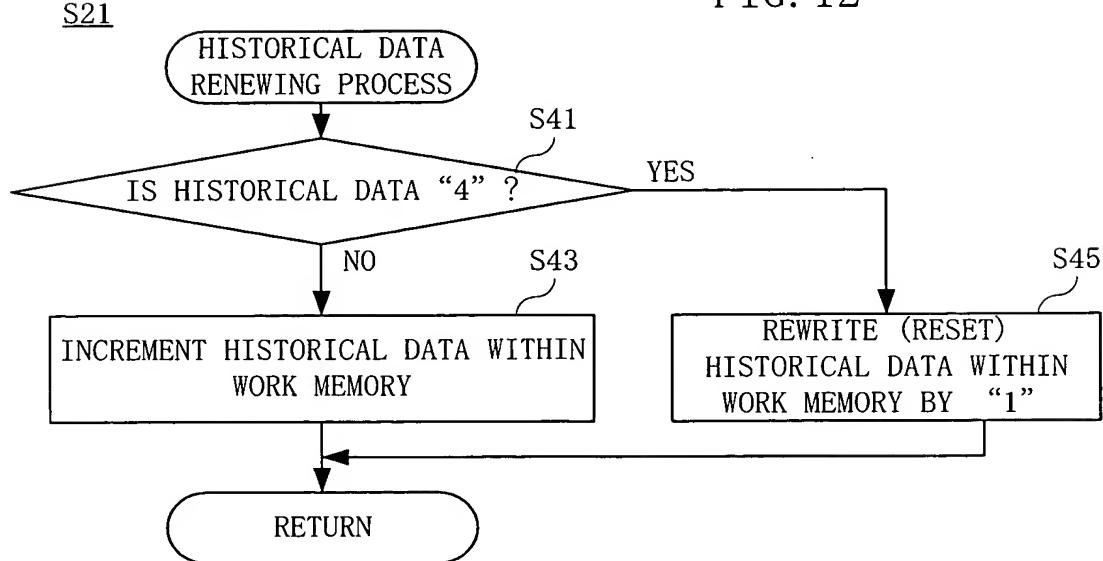


FIG. 13

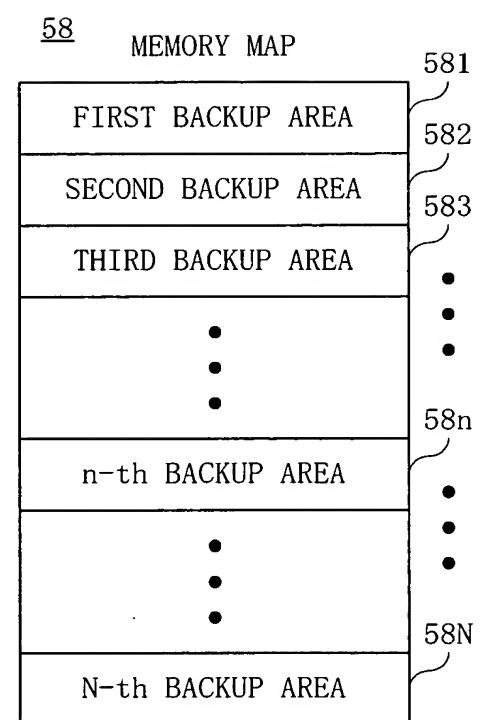


FIG. 14

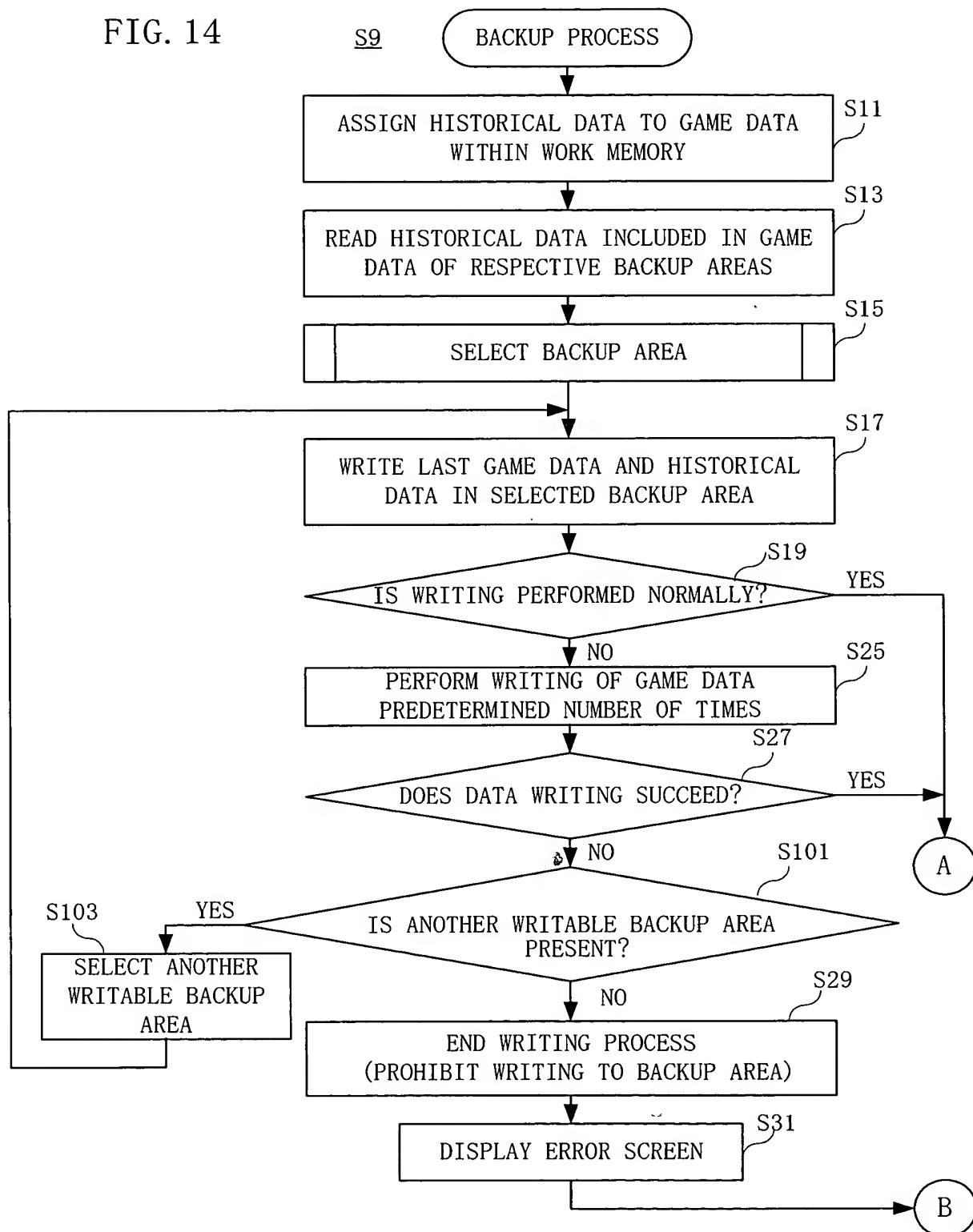


FIG. 15

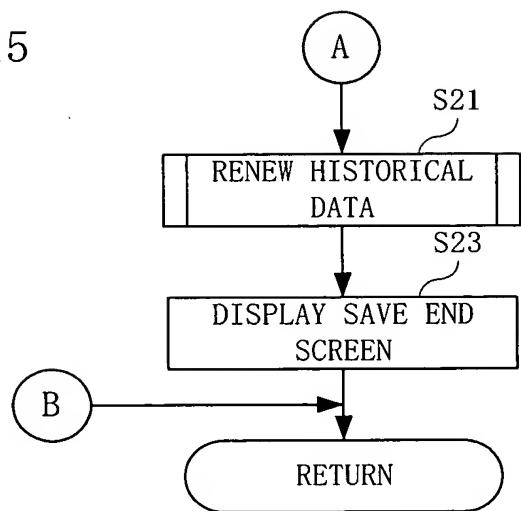


FIG. 16

